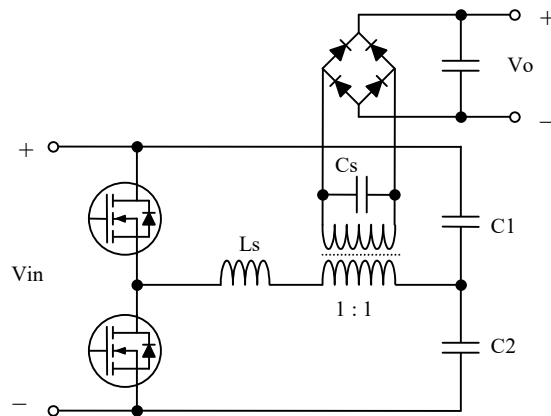


Resonance half bridge LCC converter analysis

► Main iteration routine



Basic half bridge resonance LCC converter.

Logarithmic frequency sweep:

Min. frequency:

$$f_{\min} \equiv 50 \cdot 10^3$$

Max. frequency:

$$f_{\max} \equiv 400 \cdot 10^3$$

Number of steps pr. decade:

$$S \equiv 100$$

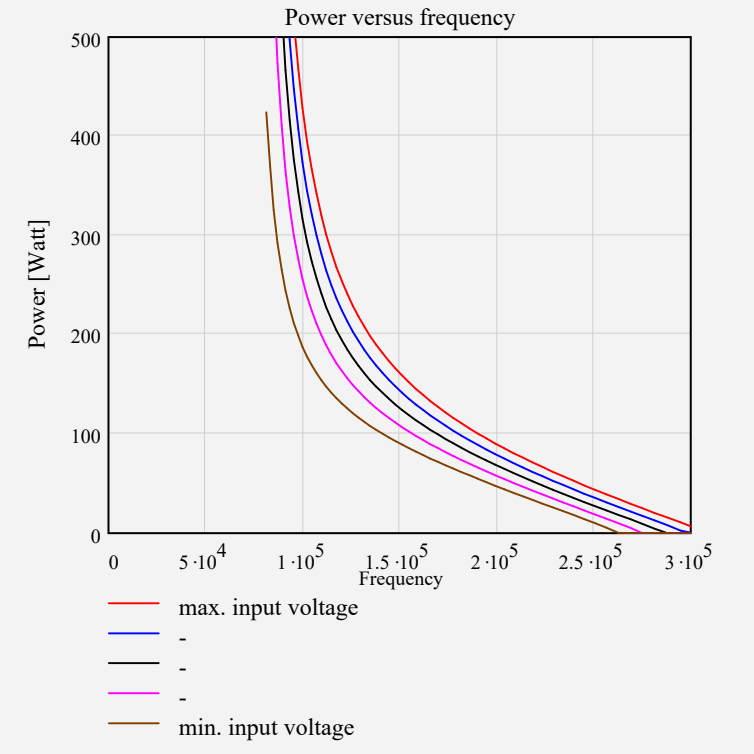
Plot to max. power:

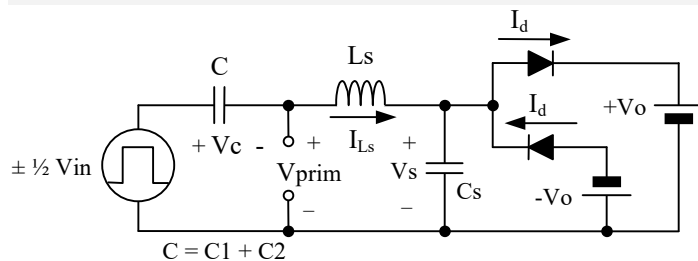
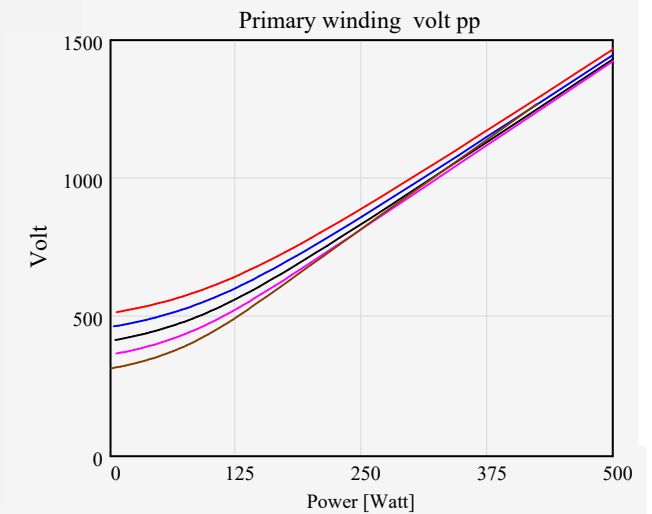
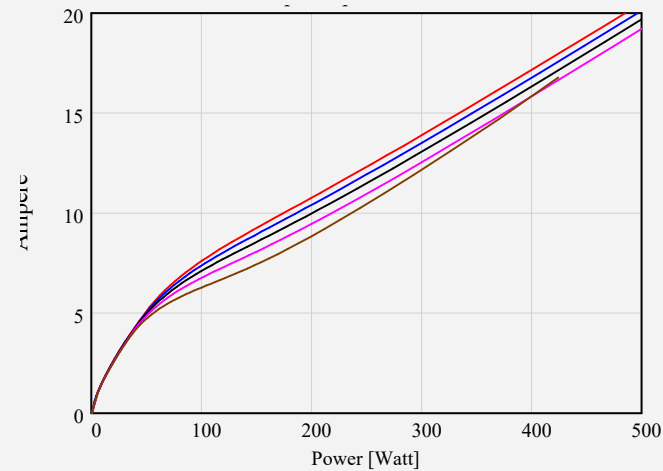
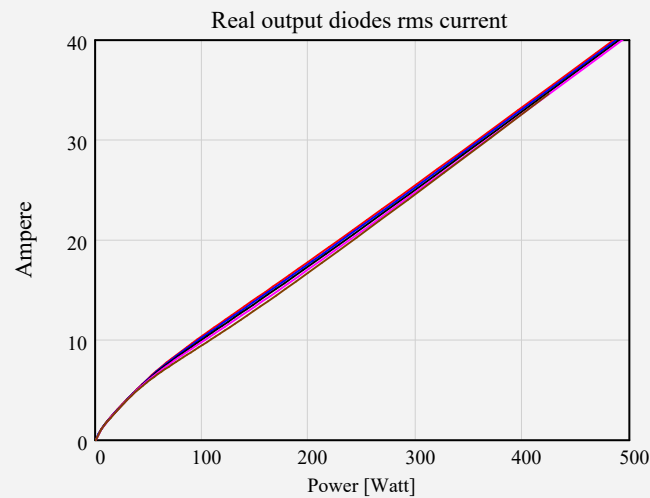
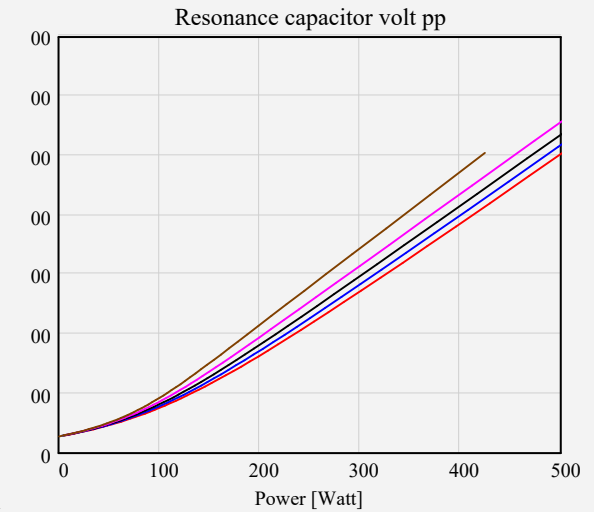
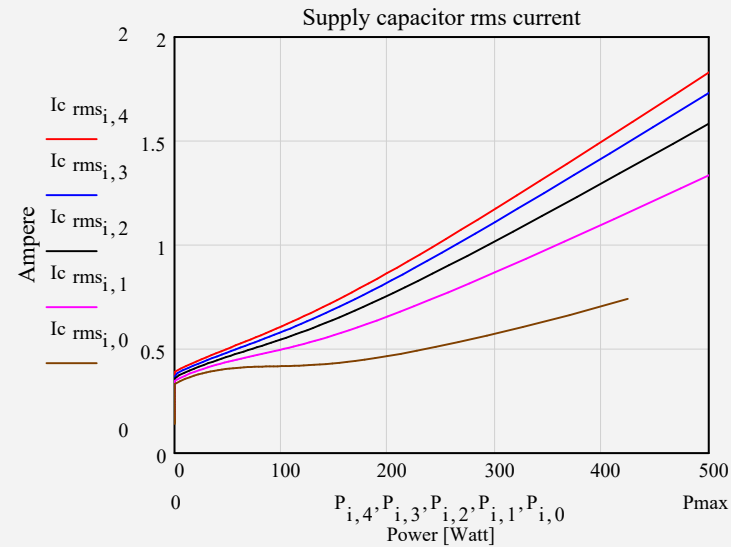
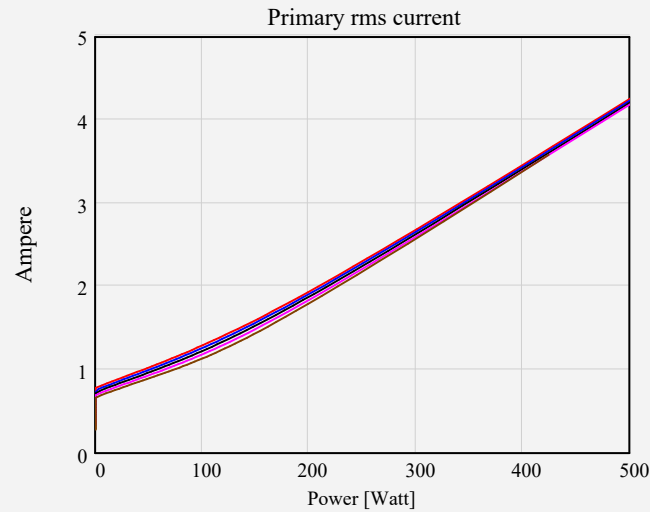
$$P_{\max} \equiv 500$$

$$i \equiv 0, 1 \dots S \cdot \log\left(\frac{f_{\max}}{f_{\min}}\right) \quad f_i \equiv f_{\min} \cdot 10^{\frac{i}{S}}$$

Input voltages:

$$\begin{pmatrix} VI_4 \\ VI_3 \\ VI_2 \\ VI_1 \\ VI_0 \end{pmatrix} = \begin{pmatrix} 460 \\ 410 \\ 360 \\ 310 \\ 260 \end{pmatrix}$$





LCC converter model

Data input:

Selected component values:

Input voltage Vin:

Fictitious output voltage (1:1 trafo):

Real output voltage:

$$C = 2 \cdot 10 \cdot 10^{-9}$$

$$V_{min} = 260$$

$$V_o = 137$$

$$V_{out} = 14$$

$$L_s = 216 \cdot 10^{-6}$$

$$V_{max} = 460$$

$$C_s = 4.073 \cdot 10^{-9}$$

Real secondary side capacitor:

$$C_s = 390 \times 10^{-9}$$

Time plots.

For check of the above results and for comparison with scope pictures or simulation currents and voltages are plotted within one switching period, in which the input voltage starts positive. Input voltage is switched between $-\frac{1}{2} V_{in}$ og $\frac{1}{2} V_{in}$.

Choose a switching frequency F and an input voltage V_{in} .
Note the "Status" sign at too low frequency.

Switching frequency:

$$F \equiv 80 \cdot 10^3$$

Input voltage:

$$V_{in} \equiv 260$$

Power

$$\text{Power} = 450.9$$

Input rms current

$$I_{i,rms} = 3.81$$

Output diodes rms current

$$I_{o,rms} = 36.88$$

Output capacitor rms current

$$I_{lyt} = 17.96$$

$$V_o = 137$$

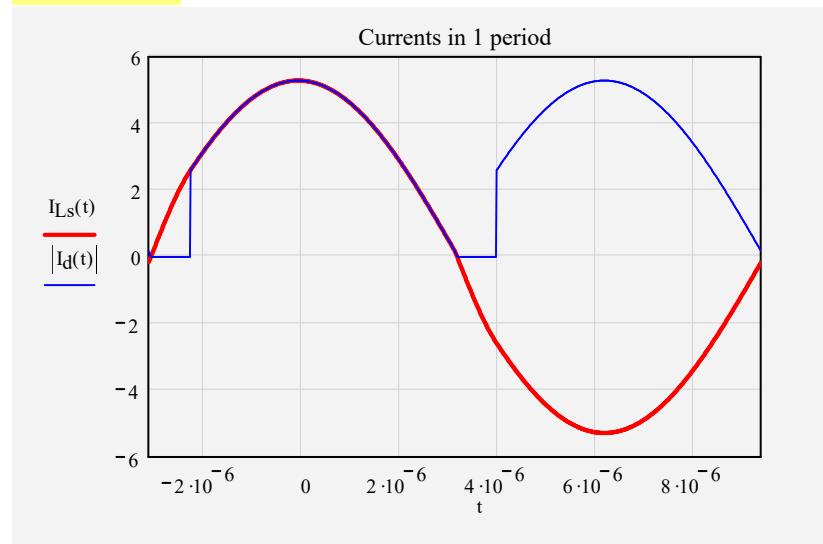
$$V_{out} = 14$$

$$L_s = 216 \times 10^{-6}$$

$$C_s = 4.07 \times 10^{-9}$$

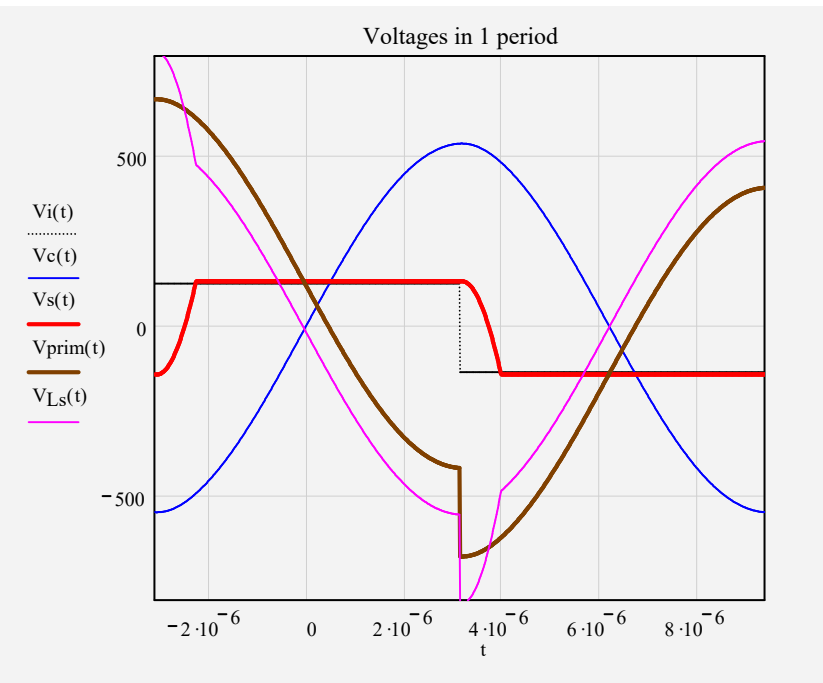
$$C = 20 \times 10^{-9}$$

Status = "OK"



$I_{Ls}(t)$: current in inductor and primary switches.

$|I_d(t)|$: current in output diodes all together (trafo = 1:1)



$V_i(t)$: input voltage.

$V_s(t)$: voltage over secondary capacitor C_s .
Will be clamped til $\pm V_o$.

$V_c(t)$: voltage over primary series capacitor C .
Will be very high near resonance !

$V_{prim}(t)$: voltage over primary winding if L_s is the leakage inductance of the transformer.

$V_{Ls}(t)$: Voltage over external inductor L_s .

Mode = 2

Transformer core and winding data input:Core cross section: $A_{fe} \equiv 173 \cdot 10^{-6} \text{ [m}^2\text{]}$ **ETD44**Core volume: $vol_{fe} \equiv 17.8 \text{ [cm}^3\text{]}$ Primary turns number: $N_p \equiv 39$ Ferrite type: $ferrite \equiv 3$

ferrite = 1 ~ 3C90
 ferrite = 2 ~ 3C94
 ferrite = 3 ~ 3C95
 ferrite = 4 ~ 3F3

copper resistivity @ 25°C [$\Omega \cdot \text{mm}$] $\rho_{cu} \equiv 17 \cdot 10^{-6}$ wire temperature [$^{\circ}\text{C}$]

diameter primary [mm]

diameter secondary [mm]

diameter primary wire [mm]

diameter secondary wire [mm]

parallel primary wires

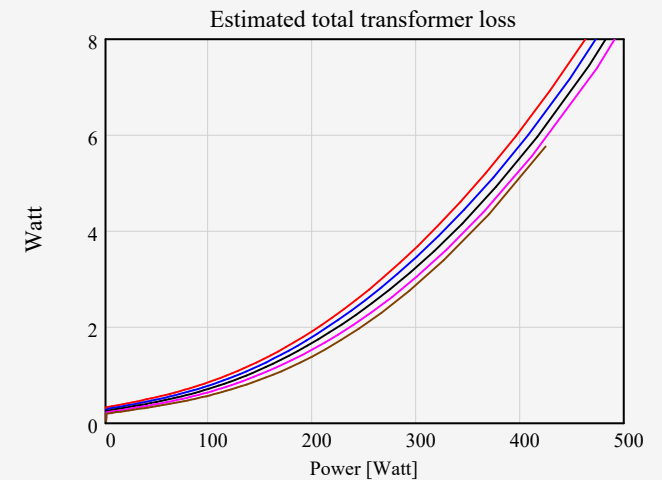
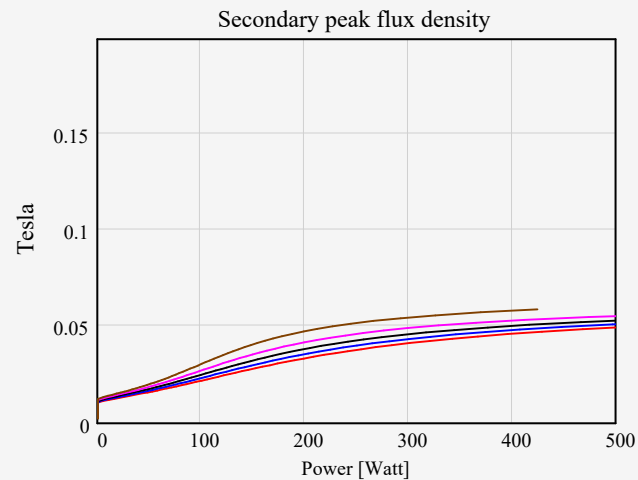
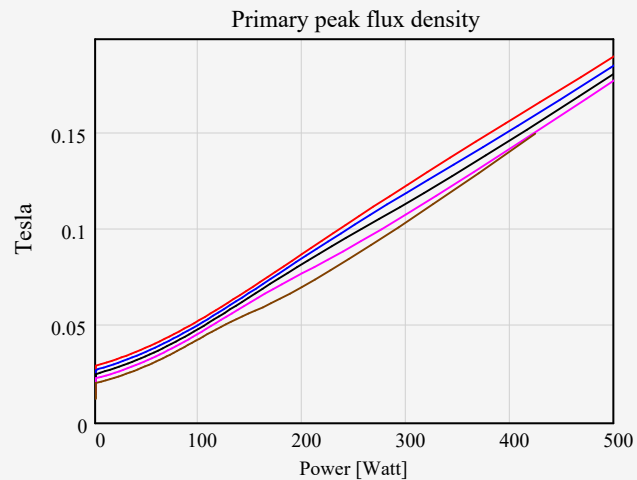
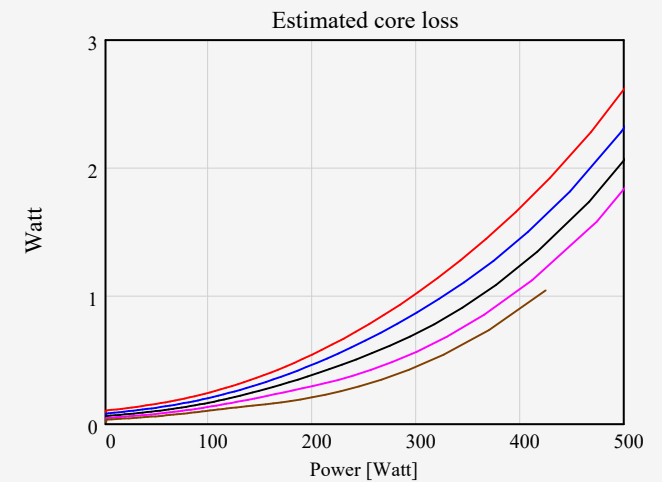
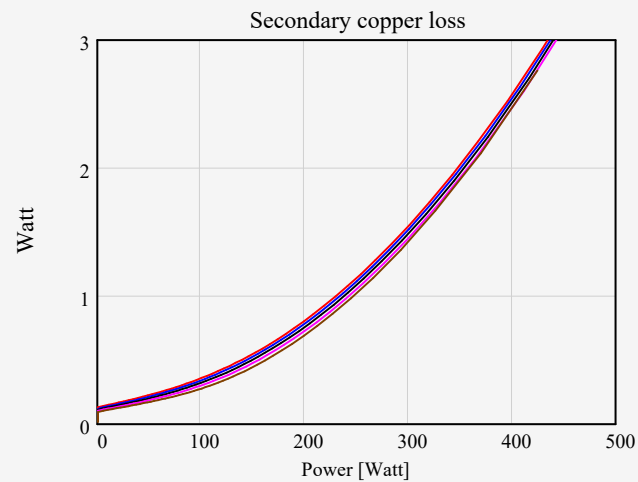
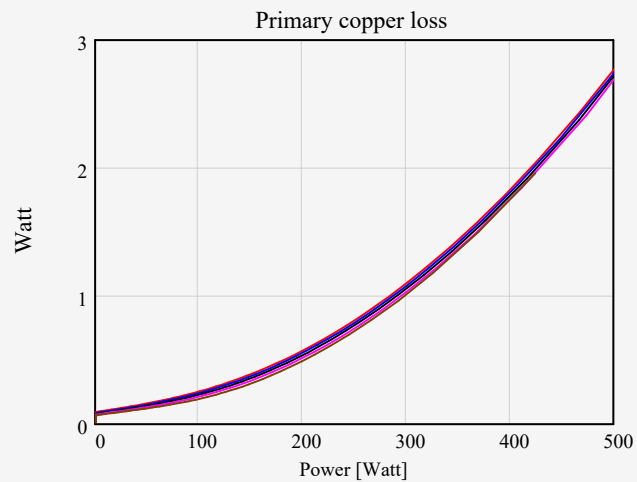
parallel secondary wires

temp $\equiv 100$ $\varnothing_{prim} \equiv 20$ $\varnothing_{sek} \equiv 20$ $\varnothing_{primwire} \equiv 0.05$ $\varnothing_{sekwire} \equiv 0.05$ parprim $\equiv 180$ parsek $\equiv 1250$ If you use external inductor on input side, write **inductor = "external"**.Otherwise L_s is integrated in the transformer:inductor $\equiv$ "internal"

Transformer turns numbers:

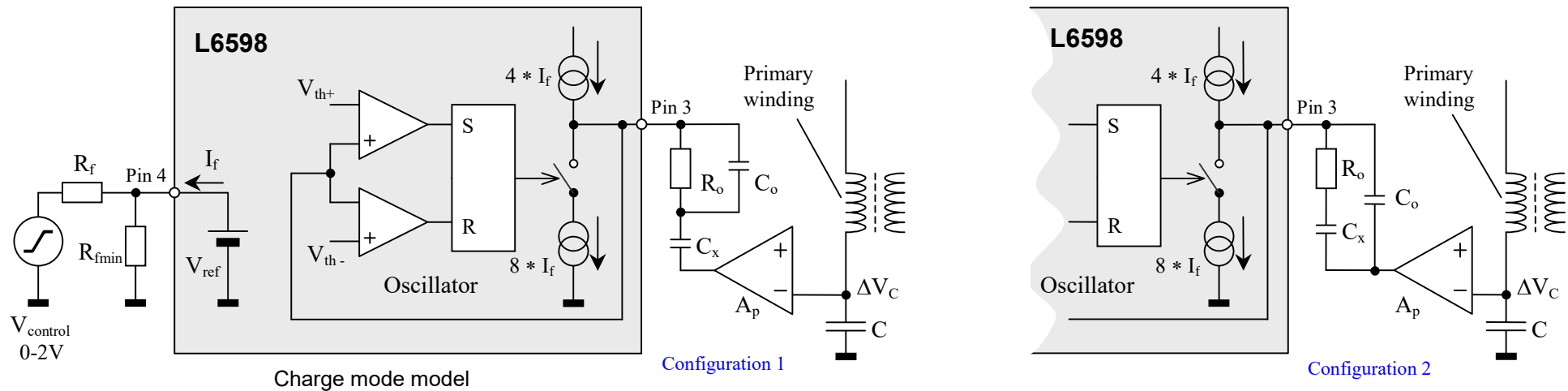
 $N_p = 39$ $N_s = 4.0$

Transformer wire resistance:

 $R_{prim} = 0.15$ $R_{sek} = 2.25 \times 10^{-3}$ 

Charge mode application with L6598.

Design of control graphs for regulation loop linearity.



Charge mode equations

Control data input:

Oscillator peak-peak voltage pin 3:

$$\Delta V_{th} \approx 2.9$$

Pin 4 reference voltage:

$$V_{ref} \approx 2$$

RC configuration - see above:

$$\text{configuration} \approx 2$$

Oscillator capacitor:

$$C_o \approx 100 \cdot 10^{-12}$$

Resistor in RC network:

$$R_o \approx 4.7 \cdot 10^3$$

DC blocking capacitor:

$$C_x \approx 1 \cdot 10^{-9}$$

Start-up resistor:

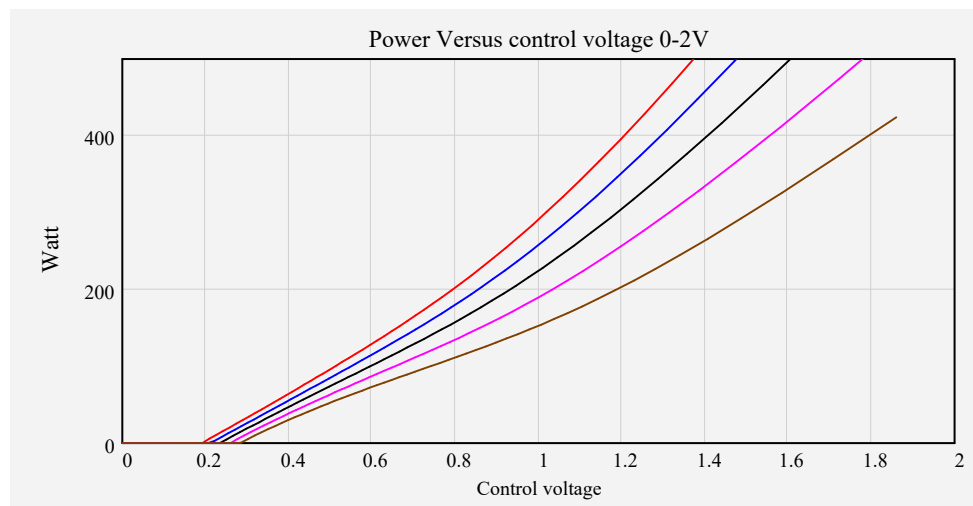
$$R_{fmin} \approx 220 \cdot 10^3$$

Frequency programming resistor:

$$R_f \approx 25 \cdot 10^3$$

Charge signal inverter gain:

$$A_p \approx 2.1 \cdot 10^{-3}$$



Input voltages:

$$\begin{pmatrix} VI_4 \\ VI_3 \\ VI_2 \\ VI_1 \\ VI_0 \end{pmatrix} = \begin{pmatrix} 460 \\ 410 \\ 360 \\ 310 \\ 260 \end{pmatrix}$$

CAUTION: with charge mode control the control voltage for no-load is quite sensitive to R_f and other parameters! Leave a good margin to $V_{control} = 0$